



High Impact Technology Exchange Conference

Educating America's Technical Workforce

Fairmont Scottsdale (Arizona)

July 19–22, 2009

Title: Teaching Digital Logic with Field Programmable Gate Arrays (FPGAs)

Date/Time: Monday, July 20, 8:30 AM – 12:00 PM

Location: Fairmont Scottsdale Resort

Description: Field Programmable Gate Arrays (FPGAs) provide an excellent mechanism for schools at the high school level or above to begin introducing the concepts of digital logic. These state-of-the-art devices provide a mechanism for students to begin to understand the concepts of digital logic and programming in a hands-on environment. The objectives of this workshop are to introduce the possibilities of creating and teaching digital logic at a school. The program that will be presented has been implemented to produce very positive results on a very limited budget for both high school and university level audiences.

Presenter:

Craig Kief, FPGA Mission Assurance Center
Ridhima Peravali, University of New Mexico



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